

# Integrated silicon carbide modulator for CMOS photonics

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**The electro-optic modulator encodes electrical signals onto an optical carrier, and is essential for the operation of global communication systems and data centers that society demands<sup>1</sup>. An ideal modulator results from scalable semiconductor fabrication and is integratable with electronics. Accordingly, it is compatible with complimentary metal-oxide semiconductor (CMOS) fabrication processes. Moreover, modulators using the Pockels effect enables low loss, ultrafast and wide-bandwidth data transmission. Although strained silicon-based modulators could satisfy these criteria, fundamental limitations such as two-photon absorption, poor thermal stability and a narrow transparency window hinder their performance. On the other hand, as a wide bandgap semiconductor material, silicon carbide is CMOS compatible and does not suffer from these limitations. Due to its combination of color centers, high breakdown voltage, and strong thermal conductivity, silicon carbide is a promising material for CMOS electronics and photonics with applications ranging from sensors to quantum and nonlinear photonics<sup>2-4</sup>. Importantly, silicon carbide exhibits the Pockels effect, but a modulator has not been realized since the discovery of this effect more than three decades ago. Here we design, fabricate, and demonstrate the first Pockels modulator in silicon carbide. Specifically, we realize a waveguide-integrated, small form-factor, gigahertz-bandwidth modulator that can operate using CMOS-level drive voltages on a thin film of silicon carbide on insulator. Furthermore, the device features no signal degradation and stable operation at high optical intensities (913 kW/mm<sup>2</sup>), allowing for high optical signal-to-noise ratios**

33 **for long distance communications. Our work unites Pockels electro-optics with a CMOS platform**  
34 **to pave the way for foundry-compatible integrated photonics.**

35 The convergence of photonics and CMOS electronics empowers photonic integrated circuits to meet  
36 the ever-increasing demand for data throughput in information systems<sup>5,6</sup>. In particular, the electro-optic  
37 (EO) modulator is at the center of this convergence as a critical component for encoding electrical signals  
38 onto light for applications in datacenters, telecommunication networks and microwave photonic systems.  
39 State-of-the-art modulators based on silicon, the workhorse material of electronics, rely on the free carrier  
40 plasma dispersion effect. This effect is intrinsically absorptive and nonlinear as it causes coupled phase  
41 and amplitude modulation, which distorts the signal modulation amplitude and restrict usage of advanced  
42 modulation formats<sup>7</sup>. Alternatively, EO modulators based on the Pockels effect i.e. linear EO effect,  
43 which exists in non-centrosymmetric crystals, allows the refractive index to vary linearly and rapidly in  
44 proportion to an applied electric field. Consequently, Pockels modulators are exploited to achieve high  
45 data rates and conversion efficiencies without the addition of optical loss<sup>8</sup>. The Pockels effect is not  
46 present in most materials in the CMOS family, including silicon and silicon nitride. Aluminum nitride  
47 (AlN) exhibits the Pockels effect with non-equal EO tensors, and has relatively low refractive index,  
48 which increases the complexity in dense optoelectronic integration<sup>9</sup>. Modifications to the crystal  
49 symmetry of CMOS materials by strain have been proposed to realize EO modulators<sup>10-12</sup>, but suffer from  
50 trade-offs fabrication complexity. For example, strained silicon<sup>11,12</sup> suffers from two-photon optical  
51 absorption and poor thermal stability that is inherent to silicon. Furthermore, the relatively small indirect  
52 bandgap of silicon prevents the usage of silicon waveguide in the visible spectrum range, which is of  
53 great interest for applications in sensing and quantum optics<sup>13,14</sup>. Commonly-used lithium niobate  
54 (LiNbO<sub>3</sub>) Pockels modulators have a strong EO coefficient<sup>15</sup>. However, they suffer from signal-distortion  
55 induced by photorefractive that worsens with increasing optical powers<sup>16</sup>, and is avoided by post-  
56 modulation amplification in applications. Moreover, low costs and high yields on chip are required, as  
57 permitted by the integration via CMOS compatible fabrication<sup>17</sup>.

58 One CMOS-compatible material that exhibits the Pockels effect is silicon carbide (SiC)<sup>18</sup>. In  
59 particular, the cubic (3C) polytype of SiC has the largest measured EO coefficient ( $\sim 2.7$  pm/V at 633  
60 nm) of all SiC polytypes and has equal elements of the EO tensor<sup>19</sup>, which simplifies optoelectronic  
61 integration. Moreover, the wide bandgap of SiC allows broadband optical transparency from ultraviolet  
62 to infrared. Despite this, a SiC-based Pockels modulator has not been experimentally demonstrated due  
63 to poor crystal quality and difficulty obtaining low-loss waveguides<sup>20–23</sup>. Although 3C-SiC can be grown  
64 directly onto a silicon substrate, it has been difficult to realize high-quality thin films, due to crystal  
65 defects associated with this approach<sup>23,24</sup>. Wafer bonding techniques<sup>25</sup> and annealing processes<sup>26</sup> have  
66 been explored to address these problems. Yet, the former demonstrates multimode waveguides while the  
67 latter still results in a high (7 dB/cm) optical loss. These issues undermine a 3C-SiC Pockels modulator,  
68 specifically one that features single-mode waveguides for stable and high extinction-ratio ring modulators  
69 or quantum applications<sup>27</sup>.

70 Moreover, due to its high electron mobility, as well as robust properties<sup>27–30</sup>, SiC is a compelling  
71 semiconductor material for next-generation CMOS electronics and a contender for realizing monolithic  
72 integration of quantum and nonlinear photonics<sup>2,3,27,31,32</sup>. By taking the advantage of its high thermal  
73 conductivity<sup>33</sup>, wide band-gap, low thermo-optic coefficient<sup>26</sup> and high refractive index ( $\sim 2.57$ )<sup>34</sup>, SiC  
74 offers the possibility of densely integrated and robust photonic devices with low fabrication costs via  
75 CMOS-compatible nanofabrication<sup>35</sup> and the potential for integration with electronics.

76 Here we present the first SiC EO modulator. Optical modulation is achieved by electrically driving a  
77 microring resonator in sub-micron-wide 3C-SiC on insulator waveguides via the Pockels effect. A  
78 microring is chosen to enable a compact device footprint ( $90 \mu\text{m}^2$ ), while maintaining high modulation  
79 performance at low voltage. The modulator is fabricated with a CMOS-compatible process and operates  
80 at a transmission rate of up to 15 gigabits per second using CMOS-level drive voltages. Importantly, we  
81 reduce the impact of polycrystal grains and waveguide surface roughness to demonstrate  $\sim 5.4$  dB/cm  
82 optical loss using a single-mode waveguide. As a result of our work, we measure the Pockels coefficient

83 (1.5 pm/V) of 3C-SiC at an infrared wavelength for the first time. Moreover, the modulator is able to  
84 operate continuously with high optical intensities of up to 913 kW/mm<sup>2</sup> without signal degradation,  
85 facilitating low-noise microwave photonics<sup>36</sup> or parametric conversion of single photons<sup>37</sup>.

86 The fabrication of the integrated 3C-SiC modulator begins with the 530 nm-thick SiC layer with a low  
87 crystal defect density (see Methods) to reduce the scattering or absorption losses and increase EO  
88 interaction. Figure 1a shows an optical micrograph of a fabricated modulator capable of operating using  
89 a CMOS digital to analog converter (DAC). The modulator consists of a pair of 3C-SiC vertical grating  
90 couplers (VGCs) as optical input and output ports, an optical waveguide ring resonator with a loaded  
91 quality (Q<sub>L</sub>) factor of 34,310 to balance modulation efficiency and bandwidth<sup>38</sup>, and microwave strip line  
92 electrodes to deliver electrical signals. The waveguides and the VGCs are structured by electron beam  
93 lithography (EBL) (see Methods), in which a typical waveguide width of 800 nm is chosen to maintain  
94 single mode operation with high optical mode confinement. The electrodes consist of a pair of ground  
95 electrodes placed next to the sides of the waveguide and a signal electrode above the waveguide (Fig. 1b).  
96 A 1-μm-thick top cladding SiO<sub>2</sub> layer is deposited to separate the electrode to the waveguide, which  
97 prevents optical loss from mode interaction with the metal. Figure 1c shows a cross sectional scanning  
98 electron micrograph (SEM) of the modulator to illustrate the geometries of the waveguide and electrodes.  
99 The electrode thickness (~500 nm) is chosen to reduce radio frequency (RF) loss due to the skin effect.  
100 As depicted in Fig. 1d, when a voltage is applied across the signal and ground electrodes, a vertical  
101 electric field is induced predominantly in the vertical direction overlapping with the optical mode to probe  
102 the Pockels effect. The ring cavity enables the phase change to be translated into an intensity modulated  
103 output, where the resonant enhancement of the modulator allows for a small device footprint and low  
104 drive voltage operation. Figure 1e shows a SEM of our etched waveguide, which can achieve a root-  
105 mean-squared sidewall roughness less than 2.4 nm<sup>26</sup> facilitating absorption limited optical loss. To  
106 qualify the optical loss, the optical spectrum of the microring resonator with critically coupled resonances  
107 operating at the telecommunication wavelengths (1569 nm - 1600 nm) is measured to reveal single mode

108 operation (Fig. 1f) with a resonance linewidth of 36.9 pm (Fig. 1g). The obtained intrinsic Q ( $Q_i$ ) is  
109 86,000 corresponding to a linear propagation loss of  $\sim 5.4$  dB/cm.

110 To characterize the maximum operational bandwidth of our fabricated modulator, we examine the EO  
111 response at an optical input power of 6.8 mW (see Methods), which shows a 3 dB bandwidth of 7.1 GHz  
112 (Fig. 2a). The bandwidth is limited by the cavity photon lifetime of 28 ps, calculated based on the  
113 measured cavity linewidth (45 pm), that corresponds to the modulation bandwidth of around 5.7 GHz.  
114 The electrode circuit of the modulator has much broader spectral response exceeding 30 GHz, as  
115 indicated in the inset of Fig. 2a. Therefore, higher bandwidths could be achieved via reducing the cavity  
116 Q factor, however this will result in a lower modulation index with the same RF signal strength<sup>38</sup>.

117 To determine the EO performance of the modulator, we use light of 1544.1 nm wavelength and drive  
118 the modulator with frequencies between 2.5 GHz and 17.5 GHz with a peak to peak drive voltage ( $V_{pp}$ )  
119 of 1V (see Methods). The generation of double sidebands seen in the optical spectrum explicitly  
120 demonstrates the resultant intensity modulation (Fig. 2b). For increasing frequency, a reduction in  
121 sideband power is observed, consistent with the roll off induced by the resonant linewidth. For the  
122 modulation frequency less than the resonator linewidth e.g. 2.5 GHz, the modulator can achieve an  
123 extinction ratio of 3dB with  $V_{pp} = 8$  V. Using the measured optical spectrum and determining the electric  
124 field strength inside the waveguide, we are able to extract the Pockels coefficient as 1.5 pm/V (see  
125 Methods) which is higher than AlN (1pm/V)<sup>9</sup>. The combination with a low permittivity ( $\sim 9.7$ )<sup>39</sup> and high  
126 refractive index<sup>34</sup> also allows for more efficient utilization of the linear EO effect in 3C-SiC over other  
127 materials, e.g. LiNbO<sub>3</sub>. With direct current (DC) voltages, a resonance shift of 0.11 pm/V (see Methods)  
128 is measured which is lower than the measured RF shift likely due to shielding caused by trapped charges  
129 in the silicon rich SiO<sub>x</sub> layer. This could be avoided by annealing or using higher purity thermal oxide.

130 To quantify the performance of our modulator for data transmission, we demonstrate low voltage  
131 operation with digital modulation (see Methods). Using a non-return-to-zero (NRZ) pseudo random bit  
132 sequence (PRBS) of  $2^7$  bits, we drive the modulator directly from a CMOS DAC operating with a  $V_{pp}$

133 ranging from 0.2 V to 2 V (Fig. 3a). Figure 3b shows the measured binary data over a period of 5 ns at a  
134 data rate of 5 Gb/s, with an optical input power of 6.8mW using drive voltages of 2 V<sub>pp</sub> and 1.2 V<sub>pp</sub>,  
135 showing that the modulator correctly modulates the light intensity according to the applied digital  
136 sequence. Figure 3c shows the modulator operates at low drive voltages and an optical input power of  
137 6.8 mW across a range of modulation speeds, with the eye-diagram quality ( $Q_E$ ) factors greater than 2.7,  
138 which lead to bit-error ratios (BERs) below the hard-decision forward error correction (HD-FEC) limit  
139 ( $3.8 \times 10^{-3}$ )<sup>40,41</sup>. While the drive voltage is reduced from V<sub>pp</sub>=2V to 1.2V, the modulator still maintains  
140 an open 5Gb/s NRZ eye diagram, allowing for successful data transmission and detection. With V<sub>pp</sub> = 2  
141 V and an optical input power of 6.8 mW, the modulator supports bit rates up to 10 Gb/s, limited by the  
142 cavity photon lifetime bandwidth (5.7 GHz).

143 The ability for modulator to handle high optical powers is important for enhancing signal to noise  
144 ratio in the growing field of microwave photonic applications<sup>4,36</sup>, as well as for quantum transduction<sup>37</sup>  
145 and nonlinear photonics<sup>27</sup>. To quantify the operation at high and continuous optical intensities, we  
146 measure the EO responses of the modulator with varied optical input power. The results are shown in  
147 Fig. 4a, in which the optical intensity within the waveguide at the resonance wavelength is calculated  
148 from the peak circulating power within the ring resonator<sup>42</sup>. It shows by increasing the optical intensity  
149 from 254 kW/mm<sup>2</sup> to 913 kW/mm<sup>2</sup>, the EO response is enhanced by 10.2 dB without evidence of signal  
150 distortion. The observed eye diagrams (Fig. 4b) also exhibit enlarged openings at 15 Gb/s with increased  
151 optical intensities, which shows improved modulation performance for digital signals and the ability to  
152 reach larger bandwidths. Moreover, Fig. 4c shows at an optical intensity of 913 kW/mm<sup>2</sup>, the  $Q_E$  factors  
153 extracted from the measured eye diagrams for all data rates at V<sub>pp</sub> = 2 V are over 2.7, confirming the  
154 operation of the modulator at high optical intensities.

155 To distinguish SiC for optical and electrical integration, we compare the material parameters of several  
156 EO modulator platform (Fig. 4d). The large thermal conductivity of 3C-SiC (490 W/(m·K)) that is almost  
157 double that of AlN and more than 12 fold larger than LiNbO<sub>3</sub>, together with the high Moh's hardness and

158 large Young's modulus, make the modulator unrivaled for high power EO applications and co-integration  
159 with CMOS electronics. Furthermore, the ultra-high breakdown field of 3C-SiC (4 MV/cm) which is 18  
160 fold higher than LiNbO<sub>3</sub> enables the possibility to integrate RF amplifiers on chip with the modulator,  
161 and makes it resistant to electro-magnetic attacks from RF bursts. Moreover, the high radiation hardness  
162 of SiC also presents advantages in harsh operating environments<sup>29</sup>.

163 We demonstrate an integrated Pockels modulator in SiC with a drive voltage compatible with CMOS  
164 electronics, a small device footprint, and continuous low-noise data modulation at high optical intensities.  
165 The SiC EO modulator opens new opportunities for direct optoelectronic integration using CMOS  
166 foundries with the benefit to various applications ranging from optical networks, chip scale interconnects,  
167 RF and microwave photonics and quantum information. Our work constitutes an essential piece in the  
168 vision of a thin film SiC platform consisting of monolithic integration of modulators, photodiodes,  
169 quantum defects and protocols to realize on chip photonics. Finally, the ability to integrate with CMOS  
170 electronics could inspire a new generation of integrated optoelectronic devices for photonic signal  
171 processing, chip-chip or intra-chip interconnects ushering in a new era of optical based electronics.

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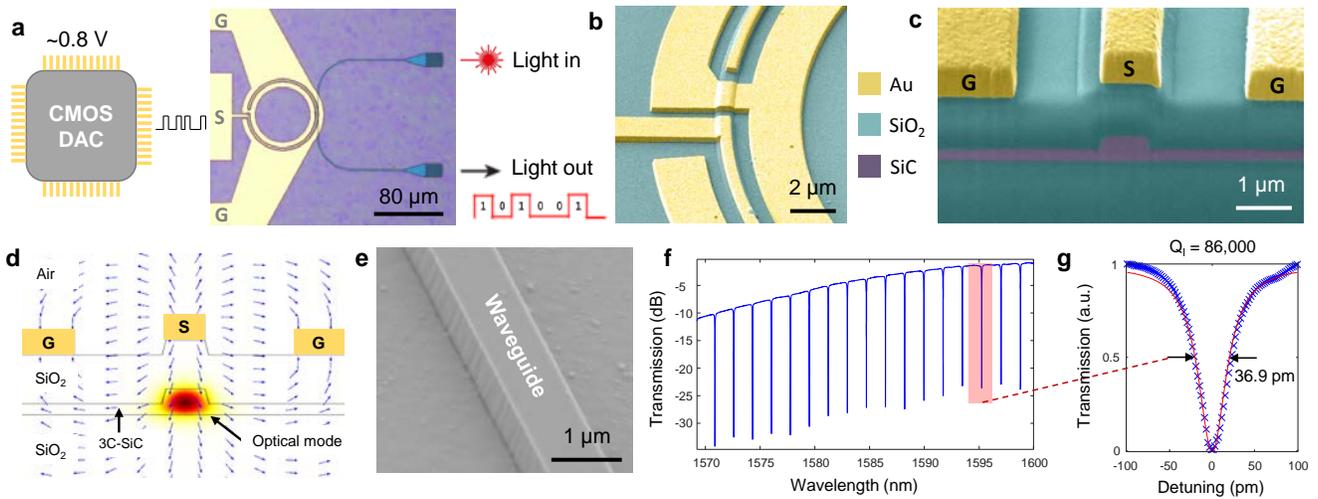
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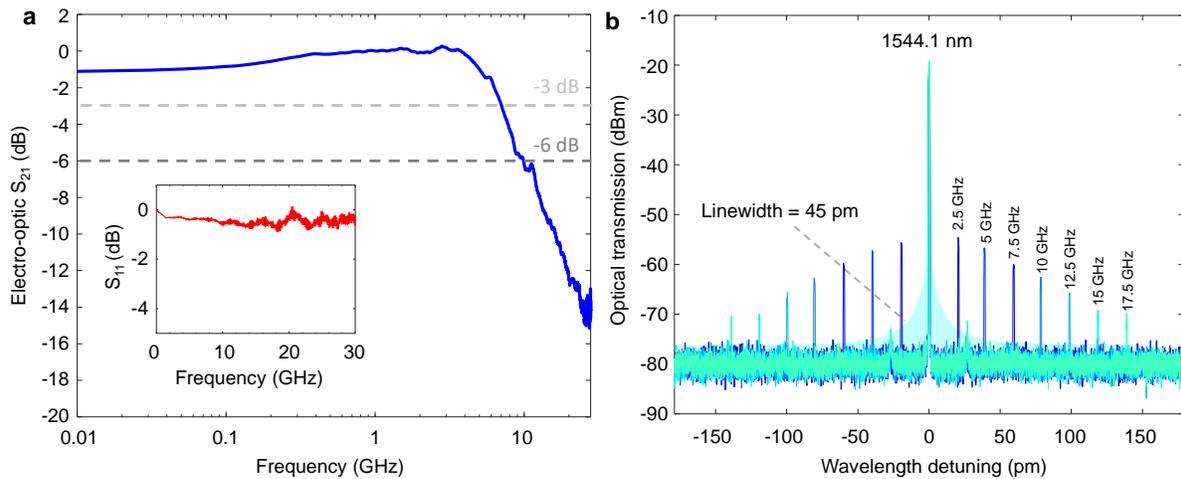
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262 **Fig. 1** Integrated Pockels modulator in SiC on insulator. (a) Overview of the fabricated ring modulator showing compatibility with CMOS voltages.  
 263 (b) False color SEM of the microring waveguide and modulator electrodes. (c) False color SEM cross-section of the active region of the modulator.  
 264 (d) Simulated static electric field and optical mode of the active region of the modulator. (e) SEM of an etched waveguide with the sidewall shown.  
 265 (f) Measured optical spectrum of the microring resonator. (g) Lorentz fit of the resonance lineshape to determine the intrinsic optical quality ( $Q_1$ )  
 266 factor. (Cross: Measurement; Solid line: Lorentz fitting)

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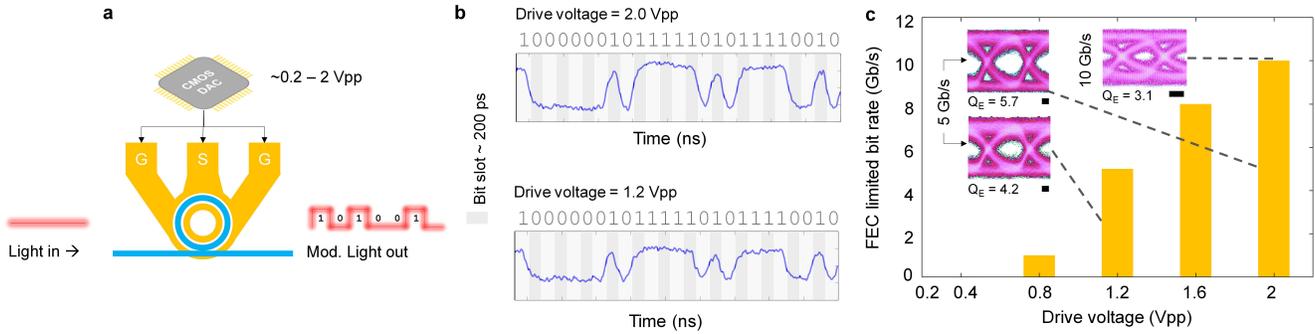
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269 **Fig. 2** Modulator bandwidth and EO characterization. (a) RF s-parameter characterization featuring a -3dB and -6dB bandwidths of 7.1 GHz and  
 270 9.9 GHz respectively.  $S_{21}$ , transmission coefficient of the scattering matrix. Inset shows the  $S_{11}$ , reflection spectrum of the modulator. (b) Optical  
 271 spectrum at the output of the modulator for various input RF frequencies. The measurement at 2.5GHz which is within the resonator linewidth is  
 272 used in the Pockels coefficient extraction.

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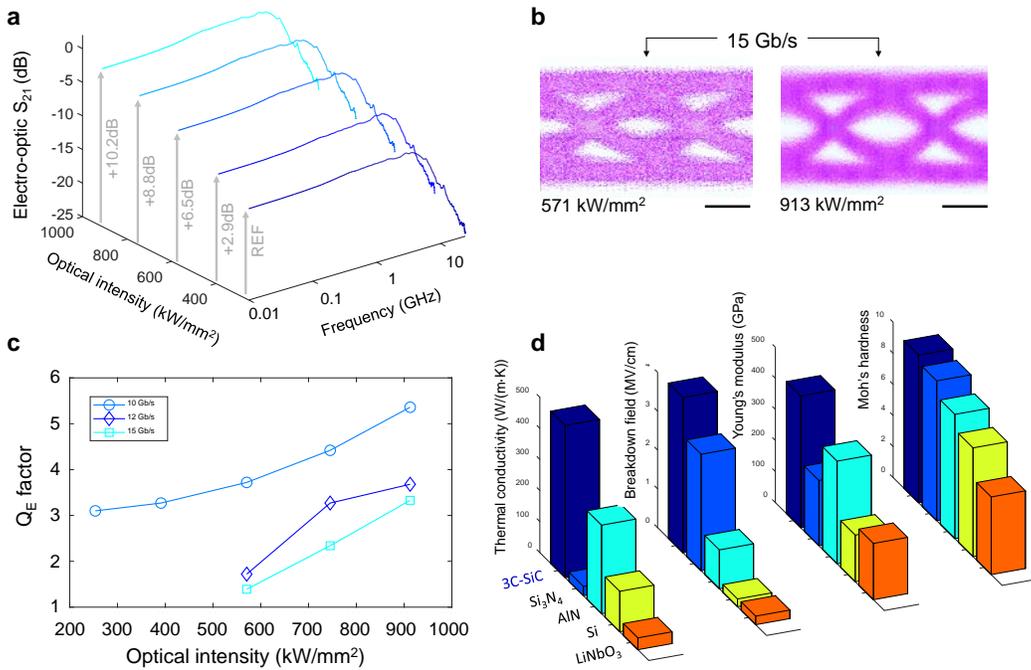
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**Fig 3.** Digital CMOS level electro-optic modulation with NRZ PRBS of  $2^7$  bits. (a) Setup configuration using a CMOS DAC to drive the ground-signal-ground (GSG) electrodes of the modulator. (b) Time domain waveforms measured at the output of the modulator at 5 Gb/s for drive voltages of 2 Vpp and 1.2 Vpp respectively. (c) Drive-voltage-dependent eye-diagram quality ( $Q_E$ ) factors for increasing bit rate. The  $Q_E$  factor greater than 2.7 corresponds to BER below the HD-FEC limit. Scale bars, 33 picoseconds.



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**Fig 4.** High power operation. (a) Electro-optic s-parameter characterization at high optical intensities showing an improvement in RF responses. (b) Measured eye diagrams at 15 Gb/s confirming the operation of the modulator at high optical intensities. (c)  $Q_E$  factors as a function of optical intensity for bit rates of 10 Gb/s, 12 Gb/s and 15 Gb/s showing an improved modulation performance for higher input intensity. (d) Material parameter comparison of 3C-SiC with widely used optical materials showing the distinct advantages of SiC for high power handling. Scale bars, 33 picoseconds.

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305

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307 fabricated the devices. J. W. and D. M. performed numerical simulations. K. P., L. L., J. D. and N. S.  
308 carried out the device characterization. K. P., N.S. and X. Y. wrote the manuscript with contribution from  
309 all authors. M. L. and X. Y. supervised the project.

310

311 **Competing interests.** None.

312

313 **Correspondence and requests for materials** should be addressed to X. Y.

314

315 **METHODS**

316 **Device fabrication.** Devices are fabricated from a commercially available 3C-SiC on silicon wafer with  
317 3.5  $\mu\text{m}$  of SiC epitaxially grown on a silicon substrate supplied by NOVASiC. A 2  $\mu\text{m}$  thick low pressure  
318 chemical vapor deposition (LPCVD) SiO<sub>2</sub> is deposited onto the SiC thin film, and the resultant stack is  
319 Van der Waals bonded to a thermal SiO<sub>2</sub> on silicon wafer before thinning down SiC via an inductive  
320 coupled plasma reactive ion etching (ICP-RIE) process. The device consists of a thin 3C-SiC layer,  
321 LPCVD SiO<sub>2</sub> and a silicon substrate for the handle. The waveguides and grating couplers are patterned  
322 on 2  $\mu\text{m}$  of hydrogen silsesquioxane (FOX-16) resist using EBL. They are subsequently etched into the  
323 SiC layer using an ICP-RIE process consisting of the CMOS foundry compatible gases SF<sub>6</sub> and C<sub>4</sub>F<sub>8</sub><sup>26</sup>.  
324 The built-up polymer due to C<sub>4</sub>F<sub>8</sub> etch gas is removed using a two-step wet cleaning process. First, a  
325 solution of hydrogen peroxide and ammonium hydroxide is used to remove the polymer. Second, a  
326 solution of hydrogen peroxide and hydrochloric acid is used to remove the metal ions from the surface  
327 of the etched waveguides. Plasma enhanced chemical vapor deposition (PECVD) process is used to  
328 deposit a 1  $\mu\text{m}$  layer of SiO<sub>2</sub> onto the fabricated devices to act as an insulation layer between the  
329 electrodes and the device, which is sufficiently thick to minimize excess absorption due to the metal  
330 electrodes. Device electrodes fabrication involves EBL patterning on Polymethyl methacrylate resist,  
331 developed with a mixed solution of 1 part Methyl isobutyl ketone and 3 parts Isopropyl alcohol. Metal  
332 layers consisting of 5 nm titanium and 500 nm of gold are deposited using electron beam evaporation  
333 followed by lift-off using a solution of N-Methyl-2-pyrrolidone.

334 **Electro-optic characterization and transmission spectrum measurement.** Laser light (Keysight  
335 81960A) around 1550 nm is amplified using an erbium doped fiber amplifier (EDFA) followed by an  
336 optical bandpass filter to reduce amplified spontaneous emission (ASE) noise. The resultant light is  
337 launched into the ring modulator (Extended Data Fig. 1a). The laser is tuned to a wavelength that matches

338 the most linear edge of a resonance to ensure minimal distortion in the modulated signal. A second EDFA  
339 is placed after the modulator to compensate for optical loss before the optical to electrical conversion via  
340 a 20 GHz photodetector (Discovery). A high-speed microwave probe (GGB) is used to deliver the  
341 modulation signal to the input port of the transmission line. To measure the EO response, the sinusoidal  
342 signal with sweeping frequency from a signal generator of a vector network analyzer (VNA, Keysight  
343 N5234A) is used to drive the modulator via the microwave probe, while the photodetector output is  
344 connected with the VNA receiver. EO response is obtained from the s-parameter of the VNA, where RF  
345 cable losses are calibrated out of the measured frequency responses. To measure the high-speed data  
346 modulation, electrical PRBS signals of voltage varied from 0.2 V<sub>pp</sub> to 2 V<sub>pp</sub> are generated from a 65  
347 GS/s arbitrary waveform generator (AWG, Keysight M8195A) and then connected with the microwave  
348 probe (Extended Data Fig. 1b). A real-time digital sampling oscilloscope with an analogue bandwidth of  
349 110 GHz (Keysight UXR series) is used to capture the received signal from the photodetector. The  
350 oscilloscope acts as the receiver for the PRBS, where a second-order phase locked loop is used for clock  
351 recovery to generate an eye diagram of the received data. The eye Q<sub>E</sub> factor was measured directly from  
352 the oscilloscope using persistence mode for a fixed number of waveforms.

353 By scanning the wavelength of the tunable laser and detecting the optical power (Keysight N7744A) at  
354 the optical output of the modulator, the transmission spectra for different DC voltages are obtained. DC  
355 bias voltages from -20V to +20V are applied on the modulator ground-signal-ground electrodes, and the  
356 results are shown in Extended Data Fig. 2.

357 **Pockels coefficient extraction.** Finite element method solver (COMSOL Multiphysics) is used to  
358 simulate the optical mode profile and the electric field distribution inside the SiC waveguide. The electro-  
359 optic overlap integral is numerically calculated to evaluate the interaction of optical and electric fields<sup>43</sup>.  
360 The modulation index is determined using the Jacobi-anger expansion method which is obtained from  
361 the optical power ratio of the modulated sideband and the optical carrier. The voltage induced effective

362 index change of the fundamental transverse electric mode is calculated from the measured resonance  
363 shift, and then the EO coefficient of the 3C-SiC waveguide at the operating wavelength is derived  
364 correspondingly<sup>38,44</sup>.

365 **Material parameter comparison.** Extended Data Table 1 lists the comparison of common photonic  
366 integration materials with 3C-SiC, specifically parameters related to power handling of the modulator.  
367 The Young's modulus and Moh's hardness are useful parameters for a broad range of applications.  
368 Moreover, the refractive index and electrical permittivity are also listed. Low electrical permittivity  
369 combined with a higher refractive index are favorable for a larger modulation efficiency at a given EO  
370 coefficient.

371 **Data availability.** The data sets generated and/or analysed during the current study are available from the  
372 corresponding authors on reasonable request.

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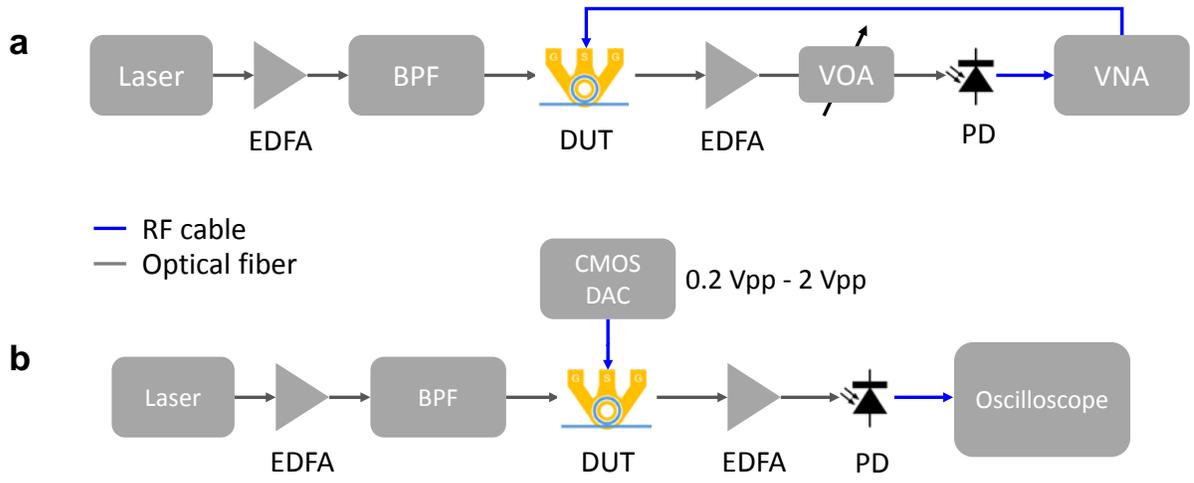
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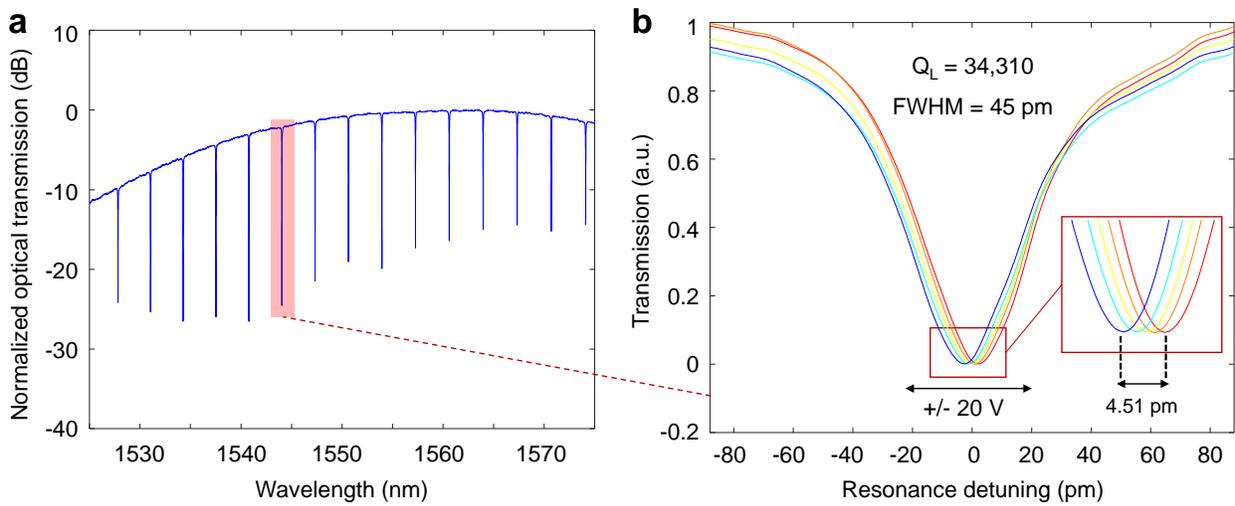
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431 **Extended Data**  
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434 **Extended Data Fig. 1** High speed measurement setups (a) Setup for measuring the EO response of the SiC modulator (b) Setup for testing the  
 435 digital communications operation of the SiC modulator. EDFA, erbium doped amplifier; BPF, bandpass filter; DUT, device under test; VOA,  
 436 variable optical attenuator; PD, photodiode; VNA, vector network analyzer.  
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439 **Extended Data Fig. 2.** Optical characterization of the electro-optic ring modulator (a) optical transmission spectrum showing single mode operation  
 440 (b) Measured DC electro-optic resonance detuning and the loaded quality ( $Q_L$ ) factor of the modulator ring resonator over a DC voltage range of  
 441  $\pm 20 \text{ V}$ . FWHM, full-width-half-maximum.  
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**Extended Data Table 1 | Comparison of photonic integration platforms for power handling and robustness**

	<b>3C-SiC</b>	<b>LiNbO<sub>3</sub></b>	<b>AlN</b>	<b>Si</b>	<b>Si<sub>3</sub>N<sub>4</sub></b>
<b>Relative permittivity</b>	9.66 <sup>45</sup>	28 <sup>46</sup>	10 <sup>47</sup>	11.8 <sup>45</sup>	5.5 <sup>48</sup>
<b>Refractive Index @ 1550 nm</b>	2.57 <sup>34</sup>	n <sub>o</sub> = 2.21 <sup>49</sup> n <sub>e</sub> = 2.14	2.12 <sup>9</sup>	3.48 <sup>50</sup>	1.98 <sup>51</sup>
<b>Moh's Hardness</b>	9.5 <sup>52</sup>	5 <sup>53</sup>	8 <sup>54</sup>	7 <sup>55</sup>	9 <sup>56</sup>
<b>Breakdown field</b>	4 MV/cm <sup>57</sup>	220 kV/cm <sup>58</sup>	1 MV/cm <sup>9</sup>	200 kV/cm <sup>45</sup>	3 MV/cm <sup>59</sup>
<b>Thermal conductivity</b>	490 W/(m·K) <sup>45</sup>	38 W/(m·K) <sup>60</sup>	285 W/(m·K) <sup>9</sup>	130 W/(m·K) <sup>9</sup>	30 W/(m·K) <sup>61</sup>
<b>Thermo-optic coefficient</b>	5.8×10 <sup>-5</sup> K <sup>-1</sup> <sup>26</sup>	3.9×10 <sup>-5</sup> K <sup>-1</sup> <sup>62</sup>	2.3×10 <sup>-5</sup> K <sup>-1</sup> <sup>63</sup>	1.8×10 <sup>-4</sup> K <sup>-1</sup> <sup>64</sup>	2.5×10 <sup>-5</sup> K <sup>-1</sup> <sup>61</sup>
<b>Mechanical elastic/Young's modulus</b>	424 GPa <sup>65</sup>	181 GPa <sup>66</sup>	330 GPa <sup>67</sup>	150 GPa <sup>68</sup>	210 GPa <sup>69</sup>

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